

Amendment and Response

Applicant: Robert Walker

Serial No.: 10/730,445

Filed: December 8, 2003

Docket No.: 2003P54211US/I331.125.101

Title: CHIP TO CHIP INTERFACE

REMARKS

The following remarks are made in response to the Office Action mailed August 31, 2005. Claims 1-13, 15, 16, 20-22, 26, 27, and 31 have been allowed. Claims 17, 18, 24, 25, and 28-30 were rejected. With this Response, claims 17, 18, 25, and 28 have been amended. Claims 24 and 29 have been canceled. Claims 1-13, 15-18, 20-22, 25-28, 30, and 31 remain pending in the application and are presented for reconsideration and allowance.

Specification

The Specification has been amended to correct the GHz to Gbit for the DDR data signals. No new matter has been added with this amendment.

Claim Rejections under 35 U.S.C. § 112

In the Office Action, the Examiner rejected claims 24, 28, and 29 under 35 U.S.C. § 112, first paragraph, as failing to comply with the written description requirement.

Claims 24 and 29 have been canceled. Claim 28 has been amended to remove the masking a clock signal limitation.

In view of the above, Applicant respectfully submits that the rejection of claim 28 under 35 U.S.C. § 112 should be withdrawn. Allowance of claim 28 is respectfully requested.

Claims 17 and 18 are rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 17 has been amended to change GHz to Gbit. Claim 18 has been amended to change 1.6 GHz to 2 Gbits as specified in the specification on page 4, line 4.

In view of the above, Applicant respectfully submits that the above rejection of claim 17 and 18 under 35 U.S.C. § 112 should be withdrawn. Allowance of claims 17 and 18 is respectfully requested.

Claim Rejections under 35 U.S.C. § 102

The Examiner rejected claims 24, 25, 28, 29, and 30 under 35 U.S.C. § 102(e) as being anticipated by Choi, U.S. Patent Application Publication No. 2004/0207544 ("Choi").

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Claims 24 and 29 have been canceled. Amended claim 25 recites **means for receiving the first signal and toggling a first bit based on a comparison of the first signal to a first reference voltage; and means for receiving the second signal and toggling a second bit based on a comparison of the second signal to a second reference voltage.** These limitations are similar to the limitations of independent claim 26 which the Examiner has allowed.

In view of the above, Applicant respectfully submits that the rejection of claim 25 under 35 U.S.C. § 102(e) should be withdrawn. Allowance of claim 25 is respectfully requested.

Independent claim 28 and independent claim 30 recite **generating a first signal in a first chip in response to a change in first data at positive edges of a clock signal; and generating a second signal in the first chip in response to a change in second data at negative edges of the clock signal.** The Examiner submits that Choi teaches generating a first signal (TX_D1') in a first chip (202) in response to a change in first data at positive edges of a clock signal (TClk1); and generating a second signal (TX_D2') in the first chip in response to a change in second data at negative edges of the clock signal (TClk2). (Office Action, page 4). Claims 28 and 30 clearly recite that both the first signal and the second signal are generated in response to a change in data at edges of *a single clock signal*. The *first signal* is generated in response to a change in data at *positive edges of a clock signal* and the *second signal* is generated in response to a change in data at *negative edges of the clock signal*. In contrast, Choi discloses generating a first signal in response to a first clock signal TClk1 and generating a second signal in response to a second clock signal TClk2. TClk1 and TClk2 are two separate clock signals that are out of phase with each other as indicated in Figure 1B of Choi.

In view of the above, Applicant respectfully submits that the rejection of independent claims 28 and 30 under 35 U.S.C. § 102(e) should be withdrawn. Allowance of claims 28 and 30 is respectfully requested.

Allowable Subject Matter

The Examiner allowed claims 1-13, 15, 16, 20-22, 26, 27, and 31.

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CONCLUSION

In view of the above, Applicant respectfully submits that pending claims 1-13, 15-18, 20-22, 25-28, 30, and 31 are in form for allowance and are not taught or suggested by the cited references. Therefore, reconsideration and withdrawal of the rejections and allowance of claims 1-13, 15-18, 20-22, 25-28, 30, and 31 is respectfully requested.

No fees are required under 37 C.F.R. 1.16(b)(c). However, if such fees are required, the Patent Office is hereby authorized to charge Deposit Account No. 50-0471.

The Examiner is invited to contact the Applicant's representative at the below-listed telephone numbers to facilitate prosecution of this application.

Any inquiry regarding this Amendment and Response should be directed Steven E. Dicke at Telephone No. (612) 573-2002, Facsimile No. (612) 573-2005. In addition, all correspondence should continue to be directed to the following address:

Dicke, Billig & Czaja
Fifth Street Towers, Suite 2250
100 South Fifth Street
Minneapolis, MN 55402

Respectfully submitted,

Robert Walker,

By his attorneys,

DICKE, BILLIG & CZAJA, PLLC
Fifth Street Towers, Suite 2250
100 South Fifth Street
Minneapolis, MN 55402
Telephone: (612) 573-2002
Facsimile: (612) 573-2005

Date: November 29, 2005
SED:bac

Steven E. Dicke
Steven E. Dicke
Reg. No. 38,431

CERTIFICATE UNDER 37 C.F.R. 1.8: The undersigned hereby certifies that this paper or papers, as described herein, are being deposited in the United States Postal Service, as first class mail, in an envelope address to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 29 day of November, 2005.

By Steven E. Dicke
Name: Steven E. Dicke